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	Application Number				
	Filing Date				
	First Named Inventor	Tadahiko Hisano			
	Group Art Unit				
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Sheet	2	of	2		

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)			
		JP	A-60-215267		Kong K K	10/28/1985	
		JP	A-63-110947		NEC Corp.	07/16/1988	
		JP	A-6-149431		Tokyo Electric Co., Ltd	05/27/1994	
		WO	WO 93/23811		SOUTHWESTER BELL TEC.	11/25/1993	

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials <sup>*</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		80186/80188 Intel User's Manual (Nov., 1994).	
		P. Oguic, "Carte E/S externe pour port parallel", Electronique Radio Plans, No. 567, Feb. 1, 1994, pp. 43-47.	
		B.J. Freeman et al., "Microprocessor-to-microprocessor communications via an 8-bit data bus" IBM Technical Disclosure Bulletin., vol. 25, No. 10, Mar. 1983, pp. 5230-5235.	

Examiner Signature		Date Considered	
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